AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (currently amended) An apparatus, comprising:

a first buffer to store a set of data ~~from~~ provided by a data source; and

a second buffer to store a subset of the data, wherein (i) the set of data is to be provided for a data requestor from at least one of the first and second buffers ~~after~~ while the subset of the data is being overwritten in [[a]] corresponding locations in the other buffer , (ii) the subset of the data is copied between the first and second buffers, and (iii) remaining data in the set is not copied between the first and second buffers.

2. (currently amended) The apparatus of claim 1, wherein the first buffer is to receive the set of data from the data source, the subset of data is to be copied from the first buffer to the second buffer before data is overwritten in the first buffer, and [[a]] the data requestor is to receive (i) the subset of data from the second buffer and (ii) the remaining data from the first buffer.

3. (original) The apparatus of claim 2, wherein the subset of data is to be copied from the first buffer to the second buffer after the locations in the first buffer that correspond to the second buffer are filled.

4. (original) The apparatus of claim 2, wherein a data ready signal is to be provided after the first buffer is filled.

5. (original) The apparatus of claim 4, wherein the data ready signal is to be provided to an arbiter unit.

6. (original) The apparatus of claim 2, further comprising:

a de-multiplexer to receive a block of data from the data source and to sequentially store the block of data in the first buffer, wherein the subset of data comprises a number of data blocks.

7. (original) The apparatus of claim 1, wherein the apparatus is associated with at least one of: (i) a packet network, (ii) a local area network, (iii) an Ethernet network, (iv) a switch, and (v) a router.

8. (original) The apparatus of claim 1, wherein the apparatus is associated with at least one of: (i) an application specific integrated circuit device, (ii) a field-programmable gate array device, and (iii) a custom integrated circuit.

9. (currently amended) A method, comprising:

storing in a first buffer a set of data from a data source;

copying a subset of the data from the first buffer to a second buffer while other data in the set is not copied;

~~storing in a second buffer a subset of the data;~~ and

providing the subset of the data from ~~one of~~ the ~~first and~~ second ~~buffers after~~ buffer to a data requestor and the remaining data from the first buffer to the data requestor while the subset of data is being overwritten in ~~a corresponding location in~~ the ~~other~~ first buffer.

10-11. (canceled)

12. (currently amended) The method of claim [[10]] 9 , wherein said copying is performed after the locations in the first buffer that correspond to the second buffer are filled.

13. (currently amended) The method of claim [[10]] 9, further comprising:

providing a data ready signal after the first buffer is filled.

14. (original) The method of claim 13, wherein the data ready signal is to be provided to an arbiter unit.

15. (currently amended) A method, comprising:

~~receiving m-bit blocks of data from a data source;~~

~~sequentially storing the blocks in a first buffer adapted to store N blocks;~~

~~after C blocks of data have been stored in the first buffer, copying the C blocks of data from the first buffer to a second buffer adapted to store C blocks, where C is less than N;~~

~~after N blocks of data have been stored in the first buffer, providing a data ready signal;~~

~~storing additional blocks from the data source in the first buffer by overwriting locations in the first buffer; and~~

~~providing to a data requestor the C blocks of data from the second buffer and the remaining blocks of data from the first buffer~~

storing in a first buffer a subset of data from a data source;

storing remaining data in a first section of a second buffer, wherein the subset of data and remaining data comprise a set of data;

copying the subset of data from the first buffer to a second section of the second buffer without copying the remaining data; and

providing the set of data from the second buffer to a data requestor while the subset of data is being overwritten in the first buffer .

16. (canceled)

17. (currently amended) An apparatus, comprising:

a storage medium having stored thereon instructions that when executed by a machine result in the following:

storing in a first buffer a set of data from a data source,

copying a subset of the data from the first buffer to a second buffer while other data in the first buffer is not copied,

~~storing in a second buffer a subset of the data;~~ and

providing the subset of the data from ~~one of~~ the ~~first and~~ second ~~buffers after~~ buffer to a data requestor and the remaining data from the first buffer to the data requestor while the subset of data is being overwritten in ~~a corresponding location in~~ the ~~other~~ first buffer.

18. (canceled).

19. (currently amended) A switch, comprising:

an Ethernet interface; and

an arbitration system, including:

a first buffer to store a set of data ~~from~~ provided by a data source, and

a second buffer to store a subset of the data, wherein (i) the set of data is to be provided for a data requestor from at least one of the first and second buffers ~~after~~ while the subset of the data is being overwritten in [[a]] corresponding locations in the other buffer and (ii) the subset of the data is copied between the first and second buffers and the remaining data in the set is never copied.

20. (original) The switch of claim 19, wherein the first buffer is to receive the set of data from the data source, the subset of data is to be copied from the first buffer to the second buffer before data is overwritten in the first buffer, and a data requestor is to receive (i) the subset of data from the second buffer and (ii) the remaining data from the first buffer.